Cooperative Token-Ring Scheduling For Input-Queued Switches

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OUTLINE

n INTRODUCTION **n** CLASSICAL ROUND-ROBIN ARBITERS n HOL and VOQ **n** COOPERATIVE TOKEN-RING **SCHEDULING** n SIMULATION n CONCLUSIONs

INTRODUCTION

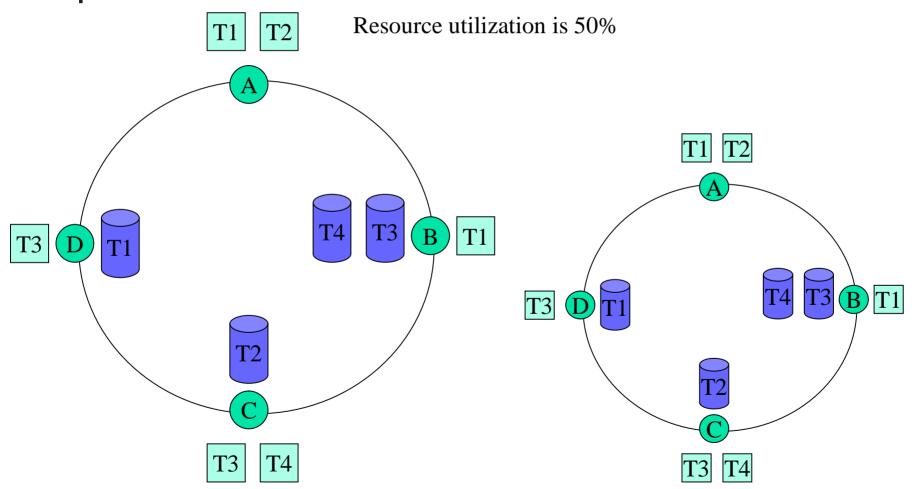
n In an $N \times N$ switch

- n Scheduling can be **optimally** solved using a <u>maximum-weight matching algorithm</u> [5], it requires a runtime complexity of $\underline{\theta}$ (N³).
- Most practical algorithms are based on <u>simple</u> <u>heuristics</u> that aim at **maximizing the number of connections between inputs and outputs** and attempt to achieve a <u>maximal match</u>.
 - n require N iterations in the worst case (hardware time complexity of $\underline{\theta}$ (logN) per iteration).
 - n Iterative for maximal matching \rightarrow Cells transferred \rightarrow Iterative for maximal matching $\rightarrow \dots$

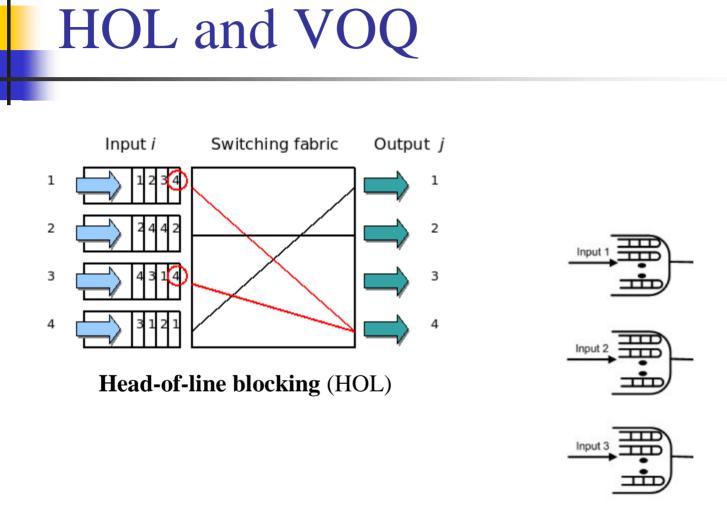
INTRODUCTION

- n Most scheduling schemes could be viewed as employing traditional token rings
 - **n** Nodes in the ring correspond to input arbiters.
 - n that perform the request and accept phases and where
 - **n** Tokens correspond to output ports.
 - A token that is acquired by a node corresponds to an input port being matched to an output port.
- n Good throughput for <u>uniform traffic</u>, but the performance degrades for realistic nonuniform traffic.

CLASSICAL ROUND-ROBIN ARBITERS



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virtual output queuing (VOQ) architecture

- n In summary, it is a challenge to find a scheduling scheme for IQ switches that meets the following requirements
 - It provides high throughput, essentially 100 percent, for both uniform and nonuniform traffic.
 - It provides rate guarantees for QoS traffic and proportional bandwidth sharing.
 - n It is readily implemented in hardware.
 - n Most practical schedulers are iterative with a hardware time complexity of θ (logN) per iteration, where N is the size of the switch;

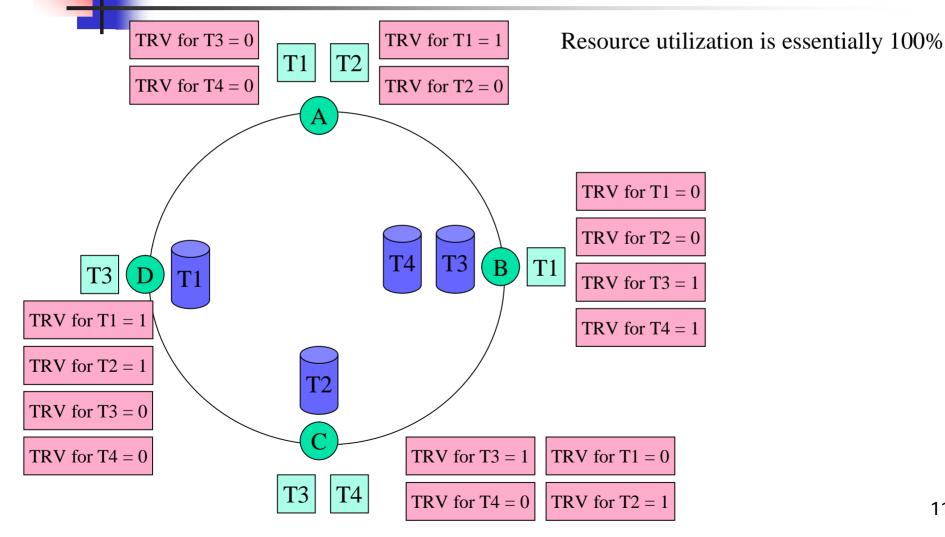
- n CTR is an **iterative scheme** such that each iteration comprises two phases.
 - n Computing the TRV (Token Request Vector)
 - Create TRVs along the ring for each token.
 - n Token propagation
 - 1. Improve a node's resource utilization.
 - 2. Improve the overall resource utilization of the ring

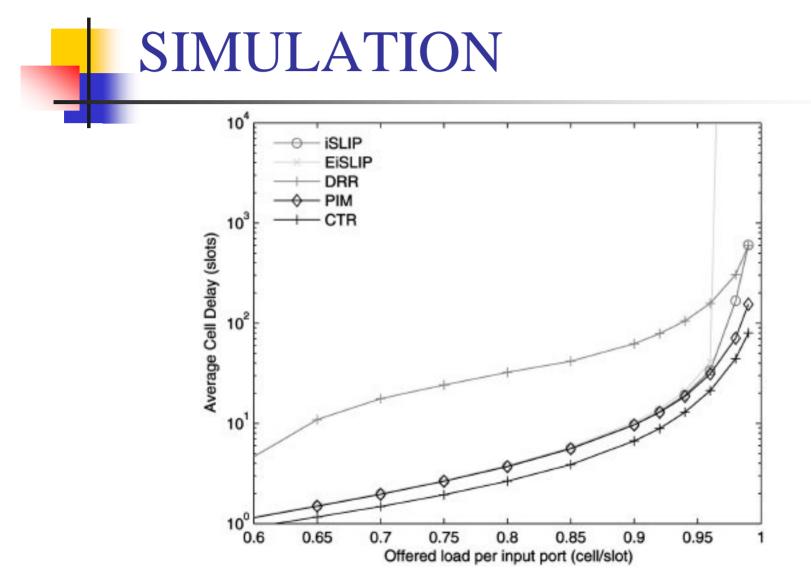
n Computing the TRV (Token Request Vector) **n** $\text{TRV}_{i,i}$: the value of the TRV at node i for token j. **True** = Exist an **unmatched downstream node** along the ring from node i that requests token j. T1 T2 t=i+N-1 $TRV_{i,j} = R_{|i+1|,j} + \sum_{t=i+2}^{\infty} (R_{|t|,j} \prod_{k=i+1}^{\infty} \overline{TP_{|K|,j}})$ **n** $TRV_{A,T1=1}$ T4 T3 B T1 T3 **n** $\text{TRV}_{A,T2=0}$ **n** $\text{TRV}_{A,T3=0}$ **n** TRV_{A T4 = 0} 9

n Token propagation

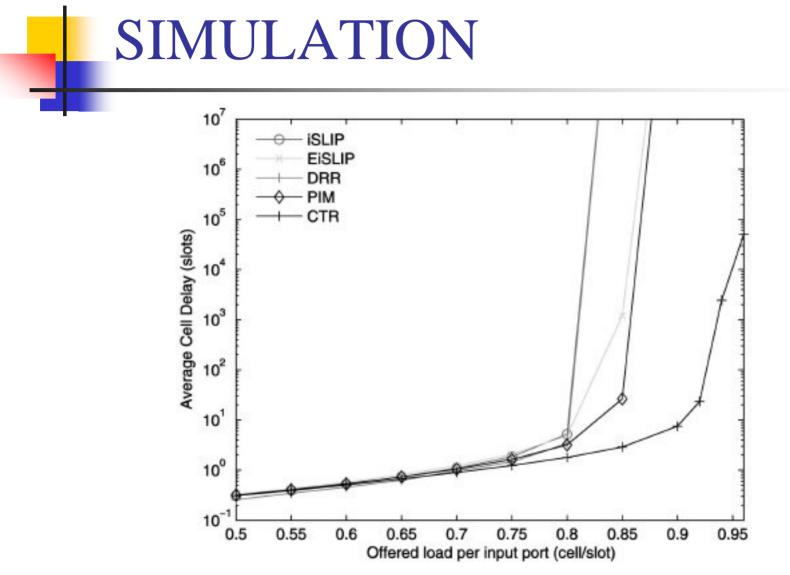
- 1. Improve a node's resource utilization.
 - An input that is **not matched** acquires the first available token <u>regardless of whether this token is critical or not</u>—this ensures that the matching converges.
- 2. Improve the overall resource utilization of the ring
 - The acquisition of a <u>critical token</u> takes **precedence** over the acquisition of a <u>noncritical token</u>.
 - n An acquired token is swapped for a critical token, when possible.



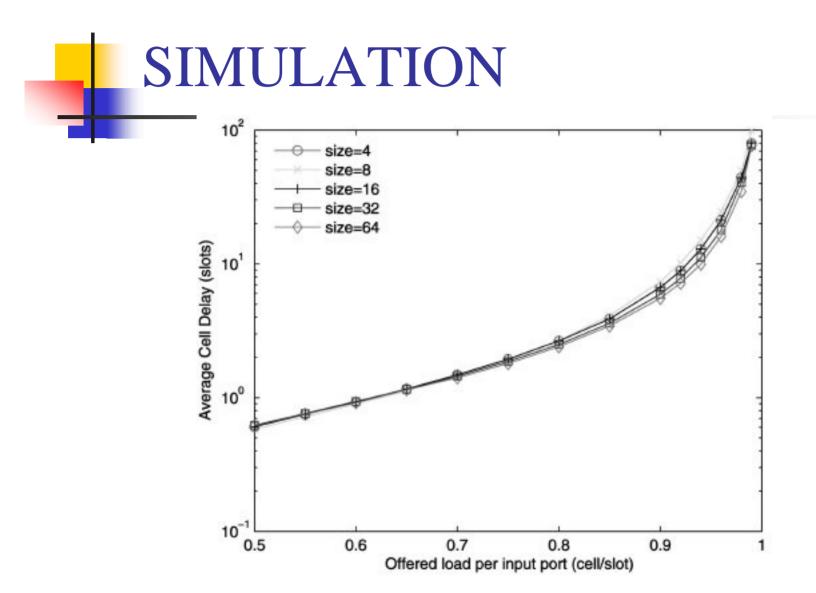




Performance of CTR, iSLIP, DRR, PIM, and EiSLIP for uniform traffic



Performance of CTR, iSLIP, DRR, PIM, and EiSLIP for diagonal traffic



The performance of CTR as a function of switch size for uniform i.i.d. Bernoulli arrivals. 14

CONCLUSIONs

- Achieves essentially 100 percent throughput for <u>uniform</u> and <u>nonuniform</u> traffic patterns.
- **n** Has a **complexity comparable** to existing iterative schedulers (time complexity of θ (logN) and a circuit size of θ (NlogN) per port).
- n Is easily implemented in hardware.
- n Generally, the proposed CTR scheme can be applied to solve any <u>resource allocation problem</u> with a set of nodes competing for exclusive access to a set of shared resources.